



501.39474X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hiroyuki MAKANO et al.
Serial No.: 09/760,704
Filed: January 17, 2001
For: **METHOD OF PROCESSING A SEMICONDUCTOR DEVICE**
Group: 2851
Examiner: Andrew T. Sever

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 17, 2004

Sir:

In response to the Office Action (Paper No. 20031013) dated on October 17, 2003, please amend the above-identified application as follows.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks/Arguments begin on page 12 of this paper.